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Relevance scale ☐ ☐ ☐ ☐ ☐**1 IBM 4341 hardware/microcode trade-off decisions**

James R. Kleinsteinber

 November 1980 **ACM SIGMICRO Newsletter , Proceedings of the 13th annual workshop on Microprogramming**, Volume 11 Issue 3-4

 Full text available: pdf(301.59 KB) Additional Information: [full citation](#), [abstract](#), [index terms](#)

The design of IBM's 4341 Processor, as with other processors, involved many cost/performance tradeoffs. The designer is continually under pressure to increase processor speed without increasing cost or to decrease processor cost without decreasing performance. This paper will examine some of the engineering decisions that were made in the attempt to make the 4341 a high-performing yet low cost processor. These decisions include searching for, or developing, algorithms that make t ...

**2 Memory optimization: Automatic data partitioning for the agere payload plus network processor**

Steve Carr, Philip Sweany

 September 2004 **Proceedings of the 2004 international conference on Compilers, architecture, and synthesis for embedded systems**

 Full text available: pdf(195.89 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

With the ever-increasing pervasiveness of the Internet and its stringent performance requirements, network system designers have begun utilizing specialized chips to increase the performance of network functions. To increase performance, many more advanced functions, such as traffic shaping and policing, are being implemented at the network interface layer to reduce delays that occur when these functions are handled by a general-purpose CPU. While some designs use ASICs to handle network functio ...

**Keywords:** network processors, partitioning, scheduling**3 Object and native code thread mobility among heterogeneous computers (includes sources)**

B. Steensgaard, E. Jul

 December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles**, Volume 29 Issue 5

 Full text available: pdf(1.50 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)


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### Patent 5339394: I/O **register** protection circuit

In addition, the width and format convertor 330 can **convert** 32 bit words to 40 bit ... The memory interface stores and **formats** data for being passed to the ...  
[www.freepatentsonline.com/5339394.html](http://www.freepatentsonline.com/5339394.html) - 65k - [Cached](#) - [Similar pages](#)

### [PPT] Avalon Imaging Codec Architecture Overview

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Built-in CODEC support for most popular image **formats** and metadata schemas ...

**Register** at installation for automatic run-time discovery. Private ...

[download.microsoft.com/download/9/8/f/98f3fe47-dfc3-4e74-92a3-088782200fe7/TWPR05004\\_WinHEC05.ppt](http://download.microsoft.com/download/9/8/f/98f3fe47-dfc3-4e74-92a3-088782200fe7/TWPR05004_WinHEC05.ppt) - [Similar pages](#)

### A Programmer's View of the SPARC Architecture (Version 7) Mark ...

... 69 basic instructions instruction **formats** - fixed 32-bit length, 3-**register**  
 ... rdwim read window invalid **mask** \* rdtbr read trap base **register** \* wry ...

[www.cs.clemson.edu/~mark/sparc/sparc\\_arch\\_desc.txt](http://www.cs.clemson.edu/~mark/sparc/sparc_arch_desc.txt) - 10k - [Cached](#) - [Similar pages](#)

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**Convert** numeric values between floating point and fixed binary or packed decimal,  
 ... PSW Format; Address Calculation; **Register** Format; Loading Addresses ...

[www.bixoft.nl/english/course4.htm](http://www.bixoft.nl/english/course4.htm) - 16k - [Cached](#) - [Similar pages](#)

### CodeGuru: Using Placeable Metafiles: Save and **Convert** to HBITMAP ...

How to save a placeable metafile, and how to **convert** it to a bitmap. Check out this short tutorial on preparing a ... Not a member? Click here to **register**) ...

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### Video YUV Transform Filter - LEAD DirectShow Filters

The output from the YUV transform can be other YUV **formats** or the following RGB

... LEAD Video Unsharp Mask Filter - Powerful algorithm for edge enhancing. ...

[www.leadtools.com/SDK/MULTIMEDIA/DIRECT-SHOW-FILTERS/Multimedia-DirectShow-YUV-Transform.htm](http://www.leadtools.com/SDK/MULTIMEDIA/DIRECT-SHOW-FILTERS/Multimedia-DirectShow-YUV-Transform.htm) - 40k - Jun 14, 2005 - [Cached](#) - [Similar pages](#)

### Aha!CAD Converter free download. CAD Converter is a format ...

It can conveniently **convert** DXF and DWG files into BMP, JPEG, DXF, DWG, etc. ...

Print out raster **mask** file; be used as image channel (used in Photoshop etc ...

[www.freedownloadscenter.com/Multimedia\\_and\\_Graphics/Graphics\\_Viewers/Aha\\_CAD\\_Converter.html](http://www.freedownloadscenter.com/Multimedia_and_Graphics/Graphics_Viewers/Aha_CAD_Converter.html) - 26k - [Cached](#) - [Similar pages](#)

### HP-42S Program to work with time and date values

This is a program to **convert** a value in the X **register** to either a date and time or days ... Toggles between the Bailey Controls and C time/date **formats**. ...

[www.wwnet.net/~stevelim/time-42.html](http://www.wwnet.net/~stevelim/time-42.html) - 26k - [Cached](#) - [Similar pages](#)

### Unit 4.1 – Translation of if-then-else PROG Maximum of Two ...

2) Set **mask** to zero 3) If we should branch when var1 = var2, add 8 to **mask** if we ... I would like to provide you with some guidance on how to **convert** IF ...

[www.wiu.edu/users/mflll/cs310/unit41.html](http://www.wiu.edu/users/mflll/cs310/unit41.html) - 10k - [Cached](#) - [Similar pages](#)